

Figure 4.17 COMPONENT LAYOUT ROM-CARTRIDGE

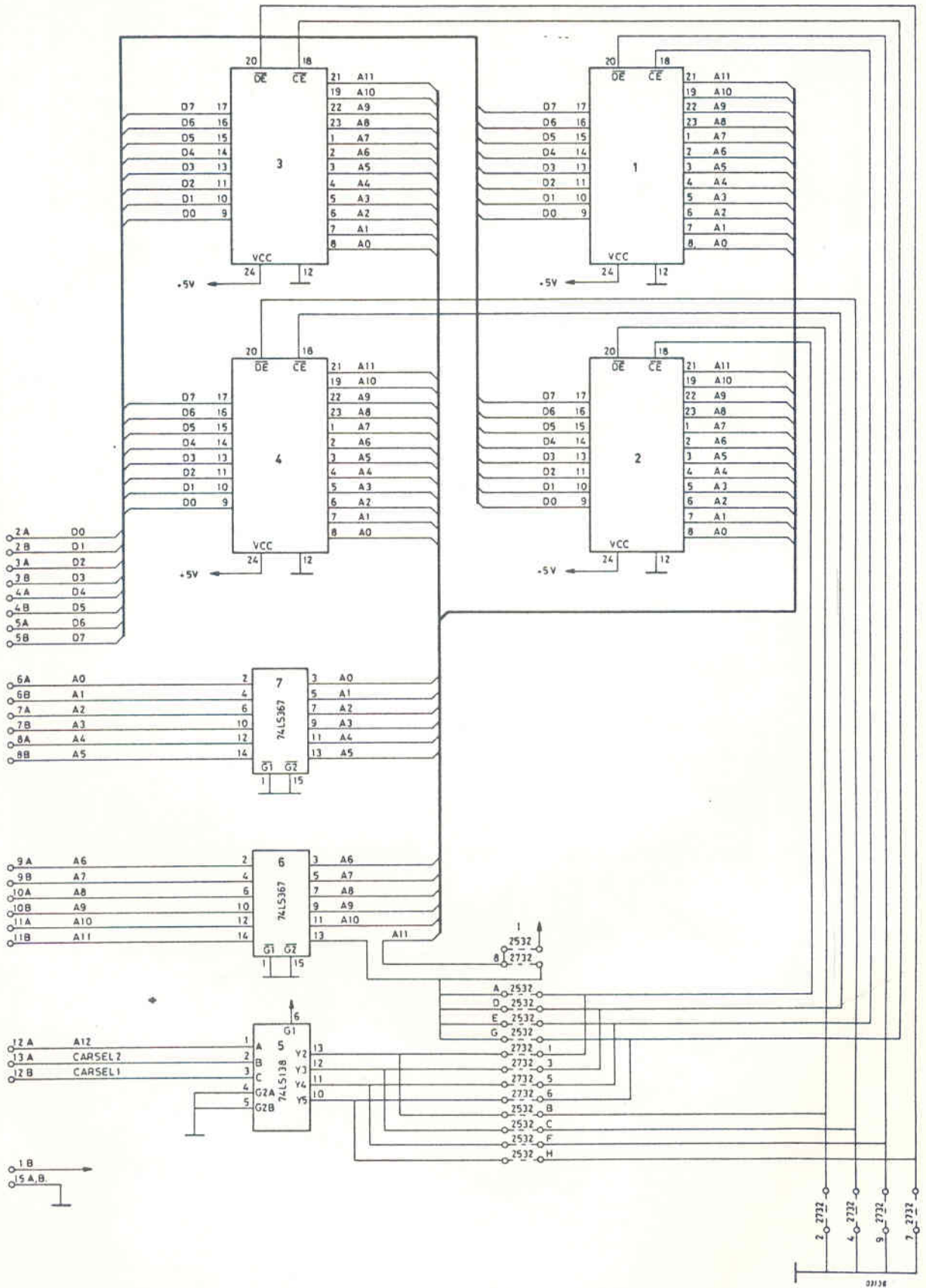


Figure 4.18 LOGIC DIAGRAM ROM-CARTRIDGE